

11050 U.S. PTO
10/047789



01/15/02

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10047789	FILING DATE 01/15/2002	CLASS 714	SUBCLASS 718	GAU 2133	EXAMINER <i>J. Kervoros</i>
**APPLICANTS: Park Jin-Young;					
**CONTINUING DATA VERIFIED: <i>none</i> <i>10/5/04</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>yes</i> <i>10/5/04</i> REPUBLIC OF KOREA 2001-34736 06/19/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH			RESCIND <input type="checkbox"/>		
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials			ATTORNEY DOCKET NO 8836-147 (IB11125-US)		
TITLE : Built-in self test circuit using linear feedback shift register					

BEST AVAILABLE COPY

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Shoots Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)